

# 深圳市华科百誉科技有限公司

## APPROVAL SHEET

### 承认书

Customer 客户名称	
Part NO. 产品型号	HK-T090B21-B3
Product type 产品内容	Mode: Transmissive type .Normally white. TFT LCD Module LCD Module: Graphic 1024RGB*600Dot-matrix
Remarks 备注栏	<input type="checkbox"/> APPROVAL FOR SEPCIFICATIONS ONLY <input checked="" type="checkbox"/> APPROVAL FOR SEPCIFICATIONS AND SAMPLE
Signature by Customer: 客户确认签章	

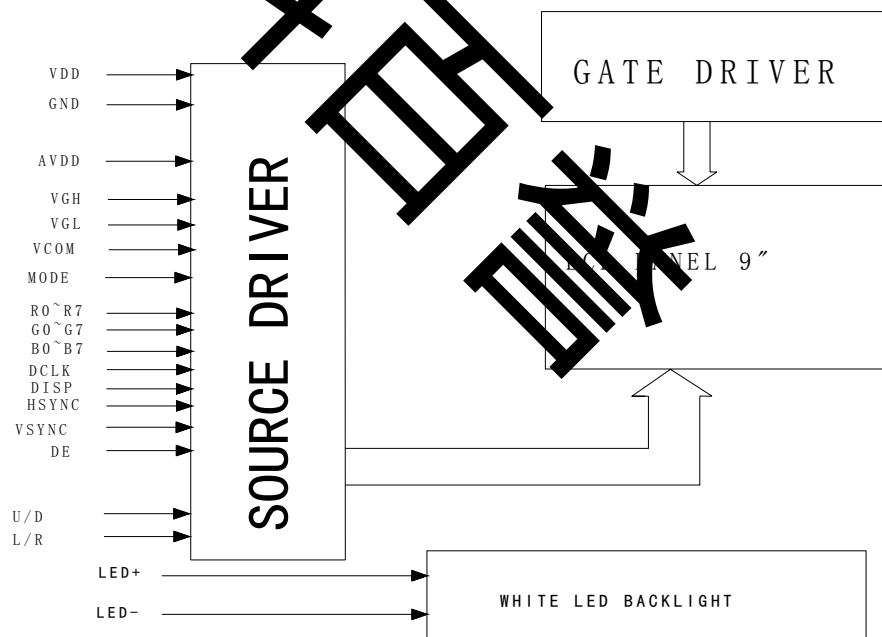
Issued by	Checked by	Approved by	
		RD	QA

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## 1. PHYSICAL DATA

Item	Contents	Unit
LCD type	TFT TRANSMISSIVE	---
Viewing direction	6	o'clock
Module size (W×H×T)	210.7× 126.4 × 3.5	mm <sup>3</sup>
Active area(W×H)	196.61×114.15	mm <sup>2</sup>
Number of dots(W×H)	1024*RGB* × 600	dots
Pixel Pitch(W×H))	0.192×0.192	mm
Colors	16M	---
Backlight Type	24 white leds 9.6V /160mA	---
Interface Type	RGB	---

## 2. BLOCK DIAGRAM



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## 3.Mechanical dimension

NO.	Pin name	NO.	Pin name
1	A1	33	R2
2	A2	34	R1
3	K1	35	R0
4	K2	36	GND
5	GND	37	CLK
6	VCOM	38	GND
7	DVDD	39	L/R
8	MODE	40	UD
9	DE	41	VGH
10	VS	42	VGL
11	HS	43	AVDD
12	B7	44	RESET
13	B6	45	NC
14	B5	46	VCOM
15	B4	47	DITHB
16	B3	48	GND
17	B2	49	NC
18	B1	50	NC
19	B0		
20	G7		
21	G6		
22	G5		
23	G4		
24	G3		
25	G2		
26	G1		
27	G0		
28	R7		
29	R6		
30	R5		
31	R4		
32	R3		

Back light circuit diagram:

\* Unspecified Tolerances is:±0.2

Note:	
LCD TYPE	9 inch TFT Transmissive
DISPLAY MODE	Normally white
VIEW DIRECTION	6 0' clock
OPERATING TEMP.	-20° C ~ 70° C
STORAGE TEMP.	-30° C ~ 80° C
BACK LIGHT	24 White leds
BL voltage/current	9.6V / 160mA
ALL MATERIALS MUST BE ROHS COMPLIAN	

UNIT: mm	SCALE: NO SCALE	SIZE: A4	
GENERAL TOLERANCE:		iA0.2	
		Angle=11.8	
DESIGNED:	CZS	DATE:	2016-11-28
CHECKED:		DATE:	
APPROVED:		DATE:	
PART NAME		LCD MODULE DRAWING	
PROJECT NO.		TG950B-B3	
PART NO.		REV: 1/1	
		REV: A	

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## 4. Pin Descriptions

Pin No.	Symbol	Functional
1	LED A	LED Anode
2	LED A	LED Anode
3	LED K	LED Cathode
4	LED K	LED Cathode
5	GND	Digital Ground
6	VCOM	For external VCOM DC input
7	DVDD	Digital Power
8	MODE	DE/SYNC mode select MODE=H: DE mode( normally pull high) MODE=L: HSD/VSD mode
9	DE	Data enable signal
10	VSYNC	Vertical sync input.Negative polarity
11	HSYNC	Horizontal sync input.Negative polarity
12~19	B7~B0	Blue data Input
20~27	G7~G0	Green data Input
28~35	R7~R0	Red data Input
36	GND	Digital Ground
37	DCLK	Clock input
38	GND	Digital Ground
39	L/R	Source right or left sequence control SHLR=H: right shift, Left → Right SHLR=L: left right, Right → Left
40	U/D	Gate up or down scan control UPDN=H: up shift, Down → Up UPDN=L: down shift, Up → Down
41	VGH	Positive Power for TFT
42	VGL	Negative Power for TFT
43	AVDD	Analog Power
44	RSTB	Global reset pin.Active low to enter reset state Suggest to connecting with an RC reset circuit for stability. Normally pull high. (RC circuit :R=10KΩ , C=1uF)
45	NC	Not connect
46	VCOM	For external VCOM DC input
47	DITHB	Dithering setting
48	GND	Digital Ground
49	NC	Not connect
50	NC	Not connect

## 5. ABSOLUTE MAXIMUM RATINGS

### 5.1 (GND=AGND=0V)

Parameter	Symbol	Min	Max	Unit
Power supply1	V <sub>DD</sub>	-0.6	+3.6	V
Power supply2	Av <sub>dd</sub>	-0.5	+13.5	V
Operating temperature	T <sub>OPR</sub>	-20	70	°C
Storage temperature	T <sub>STG</sub>	-30	80	°C

### 5.2 Input driver voltage for LCD

parameter	Typ	Unit	remark
VGH	7	V	Temperature:25 °C ±2
VGL	-6	V	
AVDD	6	V	
VCOM	3.5	V	

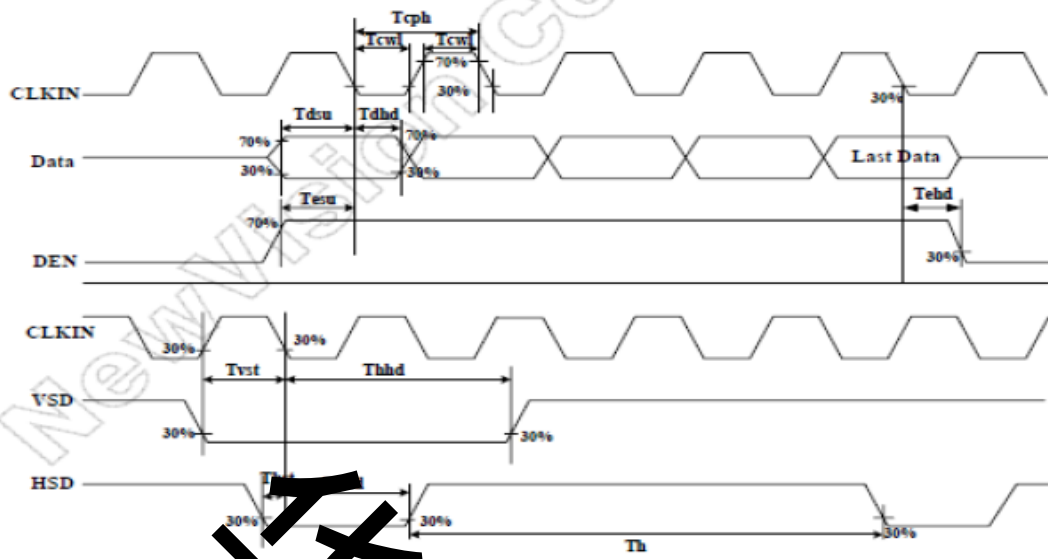
Vcom 仅供参考，按客户实际使用情况作微调至不闪

## 6. Timing characteristics

Parameter	Symbol	Spec.			Unit	Condition
		Min.	Typ.	Max.		
VDD Power On Slew rate	T <sub>POR</sub>	-	-	20	ms	V <sub>DD</sub> 0V to 90% VDD
GRB pulse width	T <sub>GRB</sub>	50	-	-	μs	F <sub>CLK</sub> =5MHz
DCLK cycle time	T <sub>cph</sub>	14	-	-	ns	-
DCLK pulse duty	T <sub>cwh</sub>	40	50	60	%	-
VSD setup time	T <sub>vst</sub>	5	-	-	ns	-
VSD hold time	T <sub>vhd</sub>	5	-	-	ns	-
HSD setup time	T <sub>hst</sub>	5	-	-	ns	-
HSD hold time	T <sub>hhd</sub>	5	-	-	ns	-
Data set-up time	T <sub>dsu</sub>	5	-	-	ns	D0[7:0], D1[7:0], D2[7:0] to DCLK
Data hold time	T <sub>dhd</sub>	5	-	-	ns	D0[7:0], D1[7:0], D2[7:0] to DCLK
DE setup time	T <sub>esu</sub>	5	-	-	ns	-
DE hold time	T <sub>ehd</sub>	5	-	-	ns	-
Output stable time	T <sub>sst</sub>	-	-	6	μs	10% to 90% target voltage. CL=90pF, R=10K ohm (Cascade) Dual gate
				3		

Table 10.1: TTL mode AC electrical characteristics

**Input Clock and Data Timing Diagram**



**7. RGB MODE DATA INPUT**

**Data input format**

**Vertical timing**

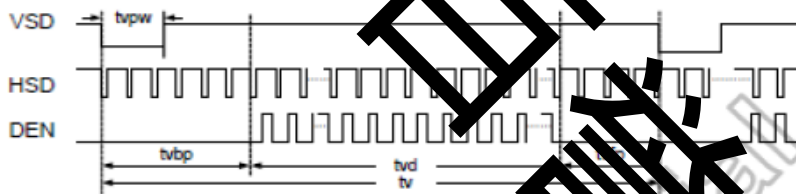


Figure 10.2: Vertical input timing diagram

**Horizontal timing**

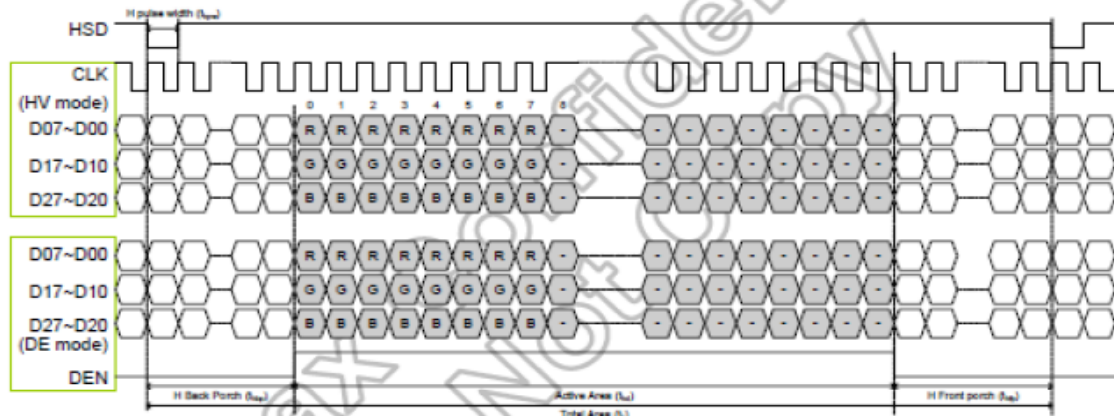


Figure 10.3: Horizontal input timing diagram

## 8. Resolution 1024x600

- DE mode

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
DCLK Frequency	fclk	40.8	51.2	67.2	MHz
Horizontal Display Area	thd	1024			DCLK
HSD Period	th	1114	1344	1400	DCLK
HSD Blanking	thb+ thfp	90	320	376	DCLK
Vertical Display Area	tvd	600			T <sub>H</sub>
VSD Period	tv	610	635	800	T <sub>H</sub>
VSD Blanking	tvbp+ tvfp	10	35	200	T <sub>H</sub>

Table 10.4: DE mode (1024x600)

- HV mode

### Horizontal timing

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
DCLK Frequency	fclk	44.9	51.2	63	MHz
Horizontal Display Area	thd	1024			DCLK
HSD Period	th	1200	1344	1400	DCLK
HSD Pulse Width	thpw	1	-	140	DCLK
HSD Back Porch	thbp	160			DCLK
HSD Front Porch	thfp	16	160	216	DCLK

Table 10.5: HV mode horizontal timing (1024x600)

### Vertical Timing

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Vertical Display Area	tvd	600			T <sub>H</sub>
VSD Period	tv	624	635	750	T <sub>H</sub>
VSD Pulse Width	tvpw	1	-	20	T <sub>H</sub>
VSD Back Porch	tvbp	127			T <sub>H</sub>
VSD Front Porch	tvfp	1	127	127	T <sub>H</sub>

Table 10.6: HV Mode Vertical Timing (1024x600)

## 9. Backlight Characteristic

Item	Symbol	Min	Typical	Max	Unit
LED module Forward voltage	V <sub>LED</sub>	--	9.6	---	V
LED module current	I <sub>LED</sub>	--	160	--	mA
L/G Surface Luminance ★1	L <sub>S</sub>	--	tbd	--	mcd
LCM Surface brightness uniform ★2	L <sub>D</sub>	80	--	--	%

★ 1 Test condition is:

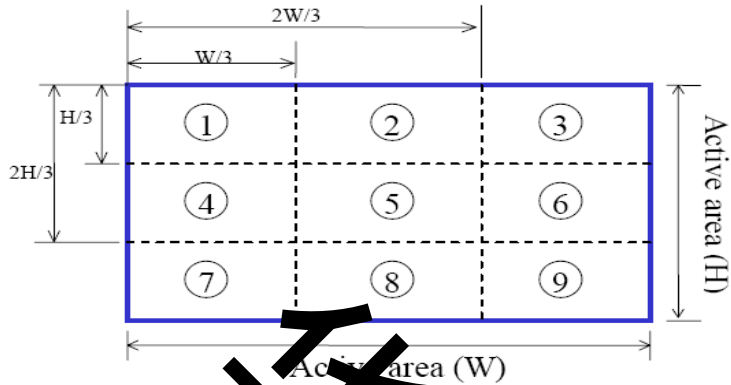
- Center point on active area.
- Best Contrast.

★2 Uniform measure condition:

(1) Measure 9 point. Measure location show below;

(2) Uniform = (Min. brightness / Max. brightness) \* 100%

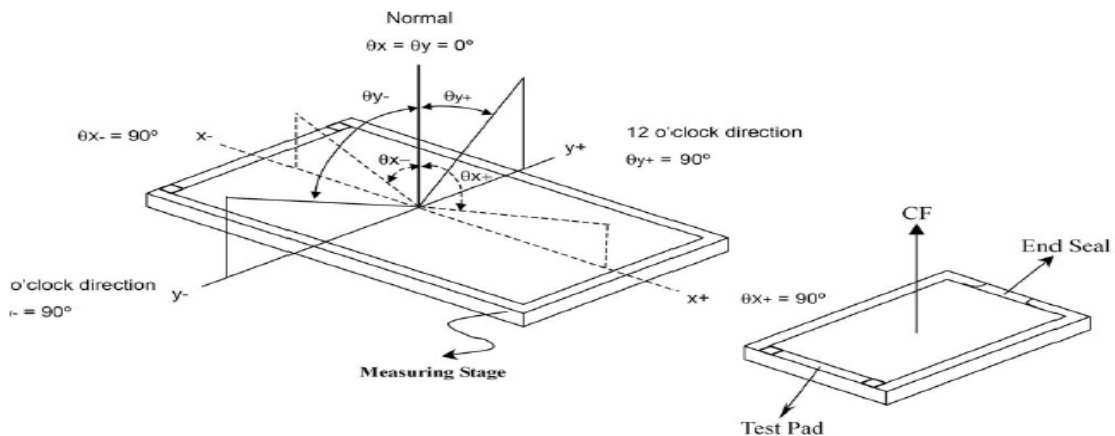
(3) Best Contrast.



10. Electro-optical Characteristics

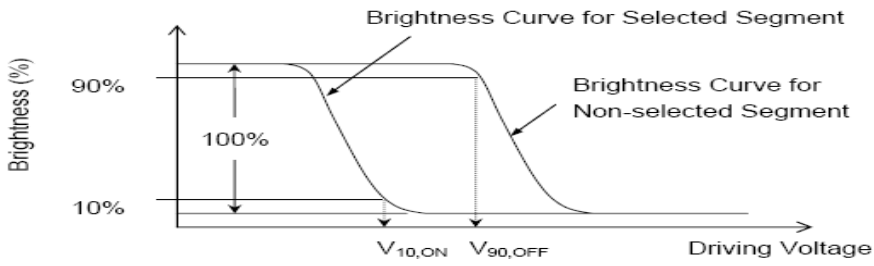
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing angle range	Hor.	$\phi 5$	60	70		Deg.	Note 3
		$\phi 9$	60	70		Deg.	
	Ver.	$\phi 12$	40	50		Deg.	
		$\phi 6$	60	70		Deg.	
Color gamut (C light)			60		%		
Luminance Contrast ratio	T (%)	$\phi 0$		100			Note 4
Response Time	TRT	Temp=25°C				ms	Note 2

- For panel only
- Electro-Optical Characteristics Test Method

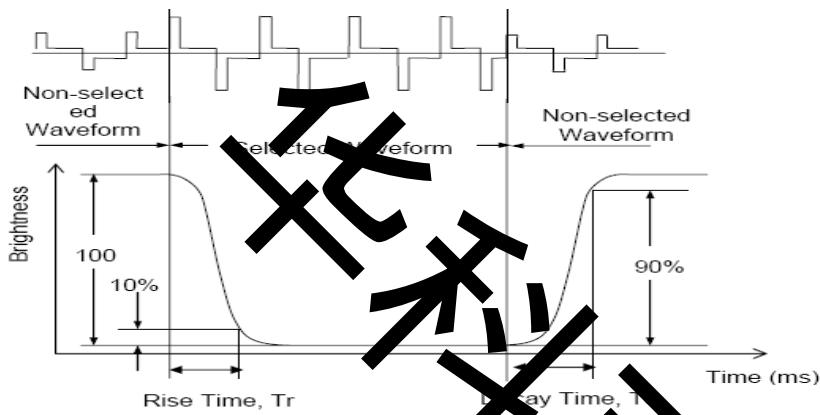




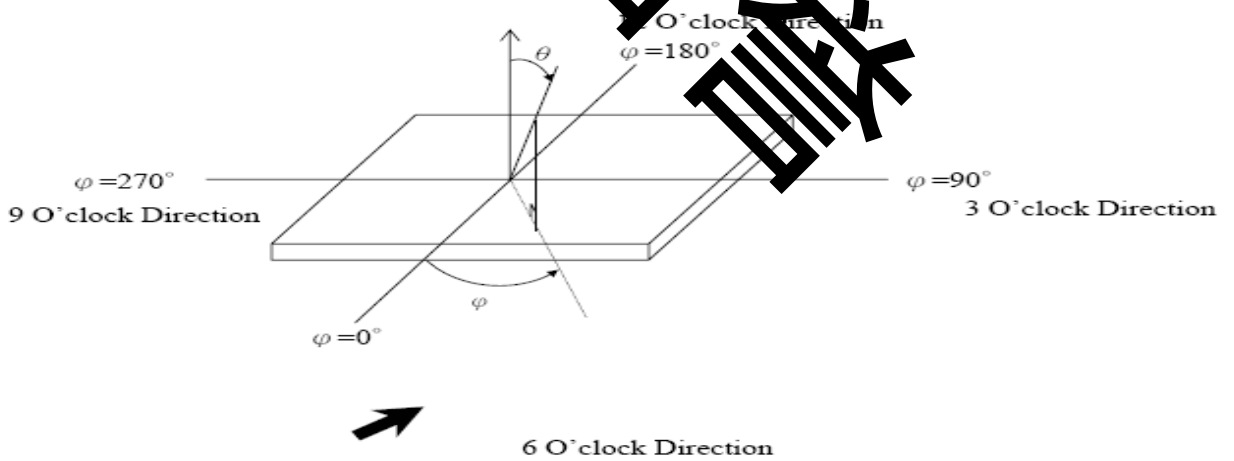
$$V_{OP} = (V_{10,ON} + V_{90,OFF})/2$$



**.Note2.Definition of Optical Response Time:**

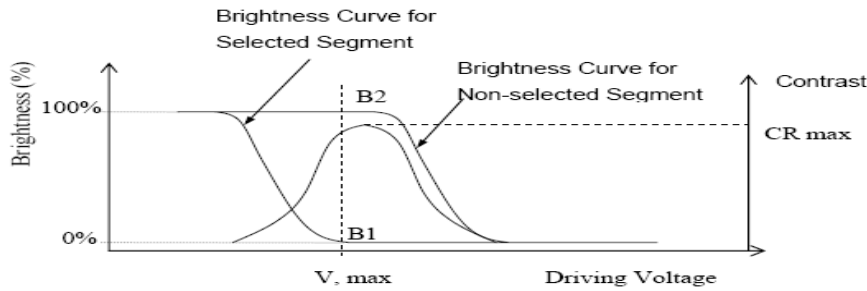


**.Note3.Definition of Viewing Angle  $\theta$  and  $\phi$ :**



**Note4.Definition of Contrast ratio (CR):**

$$CR = \frac{\text{Brightness of Non-selected Segment (B2)}}{\text{Brightness of Selected Segment (B1)}}$$



**11. Reliability**

**11.1 Mtbf**

The LCD module shall be designed to meet minimum MTBF value of 50000 hours with normal

**11.2 Test condition**

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Non-Operating Test	80°C*240Hrs	◦ No Defect Of Operational Function In Room Temperature Are Allowable
2	Low Temperature Non-Operating Test	-20°C*240Hrs	
3	High Temperature/Humidity Non Operating Test	60°C*90%RH*240Hrs	
4	High Temperature Operating Test	80°C*240Hrs	
5	Low Temperature Operating Test	-20°C*240Hrs	
6	Thermal Shock Test	-20°C (30Min) ~ 70°C (30Min) *10CYCLE	◦ IDD of LCM in Pre-and Post-Test Should Follow Specification

Notes:

- Judgments should be made after exposure in room temperature for two hours.
- The distill water is used for the high temperature/humidity test.
- The sample above is individually for every reliability tests condition.

**12. Inspection standards**

1.AQL(Acceptable Quality Level)

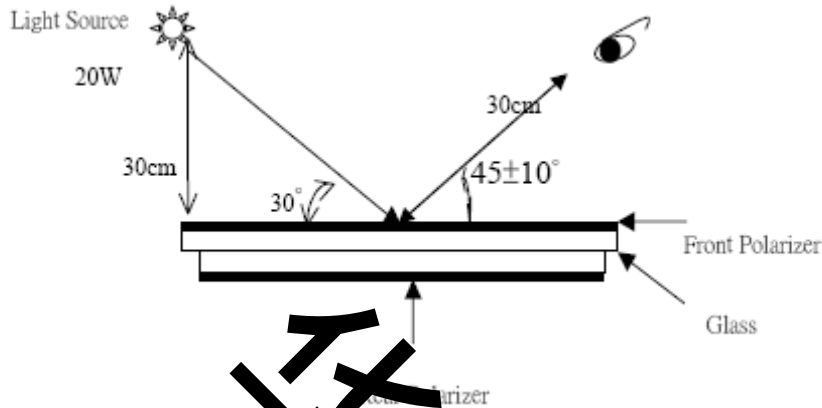
AQL of major and minor defect.

	MAJOR DEFECT	MINOR DEFECT
AQL	0.65	1.5

## 2. Basic conditions for inspection

The LCM face to us, in normal environment, the lux is  $1000 \pm 200$ . (Darkroom's lux:  $100 \pm 50$ ), About an angle of incidence  $30^\circ$ , a distance of 30 cm with an angle of  $45^\circ$  degree to check the products without uncovering the film!

(As shown below)

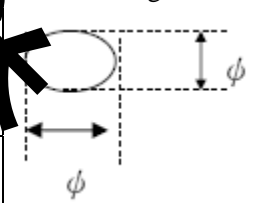
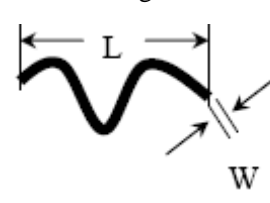


## 3. Inspection item and criteria

### 3.1 LCD appearance defect (View area)

NO	Defect item	Criteria	Remark
1	Fiber, glass cratch, polarizer scratch/folded (minor defect)	Specification	Allowable
		$W \leq 0.03\text{mm}$	disregard
		$0.03\text{mm} < W \leq 0.05\text{mm}$ $L \leq 3.0\text{mm}$	2
		$0.05\text{mm} < W \leq 0.1\text{mm}$ ; $L \leq 3.0\text{mm}$	1
		$W > 0.1\text{mm}$ ; $L > 3.0\text{mm}$	0
			note1: L: Length, W: Width note2: disregard if out of AA
2	Polarizer bubble, concave and convex (minor defect)	$\phi \leq 0.2\text{mm}$	disregard
		$0.2\text{mm} < \phi \leq 0.3\text{mm}$	2
		$0.3\text{mm} < \phi \leq 0.5\text{mm}$	1
		$0.5\text{mm} < \phi$	0
			note1: $\phi = (L+W)/2$ , L: Length, W: Width note2: disregard if out of AA
3	Black dots, dirty dots, impurities, eye winker (minor defect)	$\phi \leq 0.15\text{mm}$	disregard
		$0.15\text{mm} < \phi \leq 0.25\text{mm}$	2
		$0.25\text{mm} < \phi \leq 0.3\text{mm}$	1
		$0.3\text{mm} < \phi$	0
			note2: disregard if out of AA
4	Polarizer prick (minor defect)	$\phi \leq 0.1\text{mm}$	disregard
		$0.1\text{mm} < \phi \leq 0.25\text{mm}$	3
		$\phi > 0.25\text{mm}$	0
			note1: $\phi = (L+W)/2$ , L=Length, W=Width note2: the distance between two dots > 5mm

### 3.2 Electrical criteria

NO	Defect item	Criteria	Remark	
1	No display (major defect)	No display 【Reject】		
2	Missing line (major defect)	Missing line 【Reject】		
3	Seg-com light and dark (major defect)	Seg-com light and dark 【Reject】	ND filter 2% test	
4	No display in immobility (major defect)	No display in immobility 【Reject】		
5	Flicker of Pattern (major defect)	Flicker of Pattern 【Reject】		
6	Mura (major defect)	ND filter 2% test		
7	Over current (major defect)	Over current 【Reject】		
8	Voltage out of specification (major defect)	Voltage out of specification 【Reject】		
9	Pattern blur, error code (major defect)	Pattern blur, error code 【Reject】		
10	Dark light, Flicker (major defect)	Dark light, Flicker 【Reject】		
11	Black/white dots 、 Dirty dots、 eye winker (major defect)	Specification	Allowable	Note1:disregard if out of AA 
		$\phi \leq 0.15\text{mm}$	disregard	
		$0.15\text{mm} < \phi \leq 0.25\text{mm}$	2	
		$0.25\text{mm} < \phi \leq 0.3\text{mm}$	1	
		$0.3\text{mm} < \phi$	0	
12	Fiber、glass crutch、Polarizer scratch/folded (major defect)	$W \leq 0.03\text{mm}$	disregard	Note1:L: Length, W: Width Note2: disregard if out of AA 
		$0.03\text{mm} < W \leq 0.05\text{mm}$ $L \leq 3.0\text{mm}$	2	
		$0.05\text{mm} < W \leq 0.1\text{mm}$ $L \leq 3.0\text{mm}$	1	
		$W > 0.1\text{mm}; L > 3.0\text{mm}$	0	

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## 13.Precautions for using LCD modules.

### 13.1 Safety

- (1) Do not swallow any liquid crystal, even if there is no proof that liquid crystal is poisonous.
- (2) If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
- (3) If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

### 13.2 Storage Conditions

- (4) Store the panel or module in a dark place where the temperature is  $23 \pm 5^\circ\text{C}$  and the humidity is below  $45 \pm 20\% \text{RH}$ .
- (5) Store in anti-static electricity container.
- (6) Store in clean environment, free from dust, active gas, and solvent.
- (7) Do not place the module near organics solvents or corrosive gases.
- (8) Do not crush, shake, or hit the module.

### 13.3 Handling Precautions

- (9) Avoid static electricity, which can damage the CMOS LSI.
- (10) The polarizing plate of the display is very fragile, please handle it very carefully.
- (11) Do not give external shock.
- (12) Do not apply excessive force on the surface.
- (13) Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- (14) Do not use ketonic solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.
- (15) Do not operate it above the absolute maximum rating.
- (16) Do not remove the panel or frame from the module.

### 13.4 Warranty

The period is within twelve months since the date of shipping out under normal using and storage conditions.